



US

1/12

Fig. 1

PRIOR ART

UPSTREAM DESIGN FLOW FOR SoC

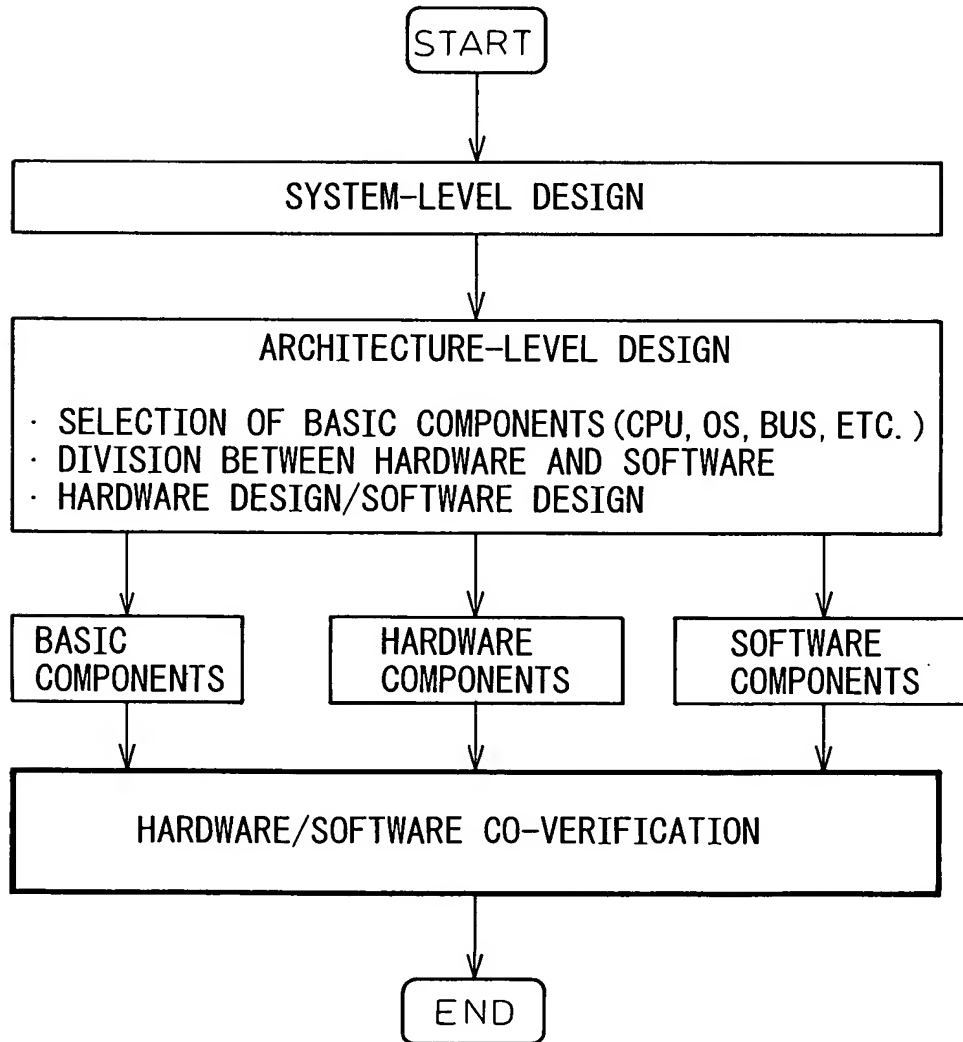


Fig.2

PRIOR ART

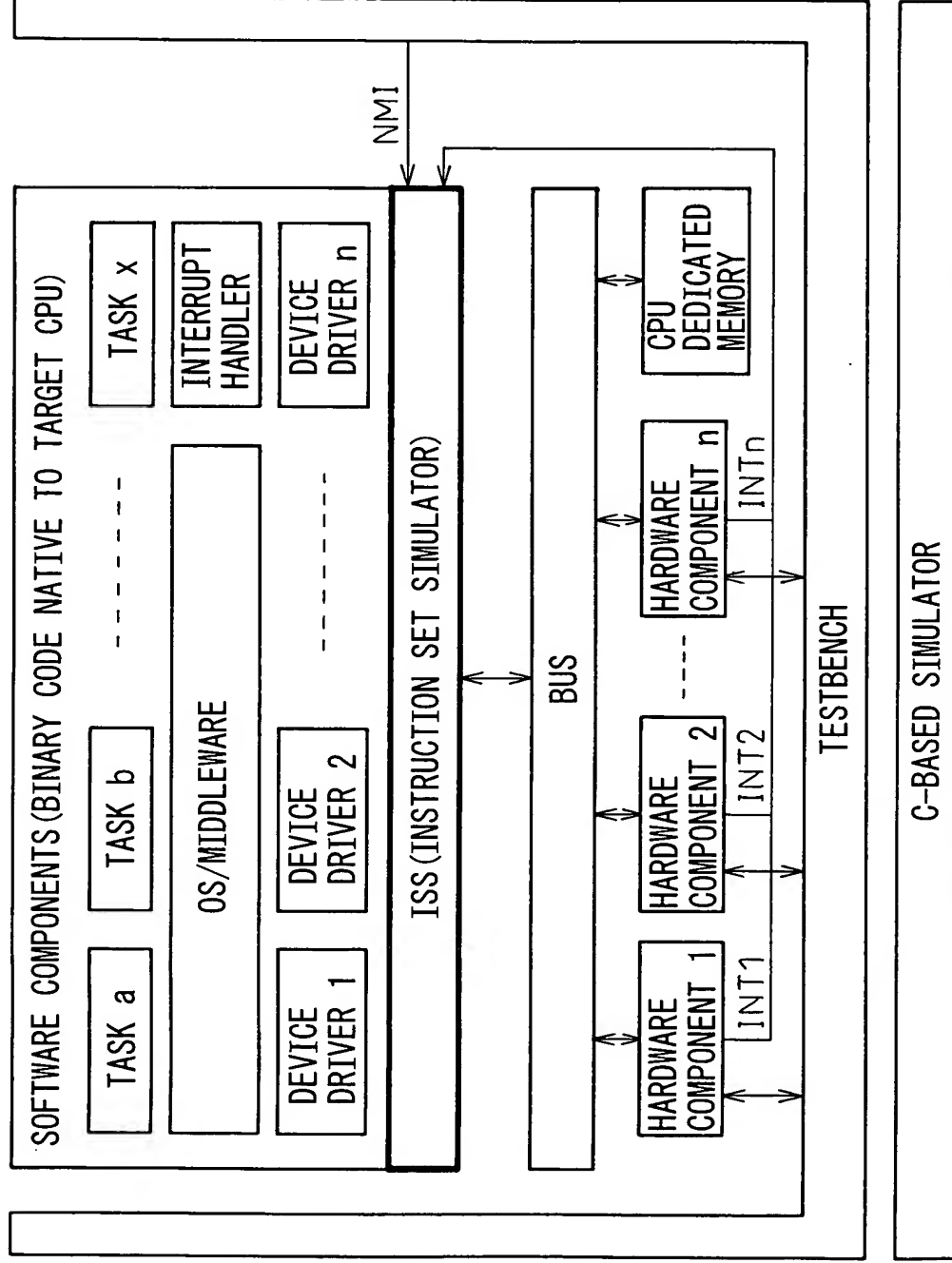


Fig. 3
PRIOR ART

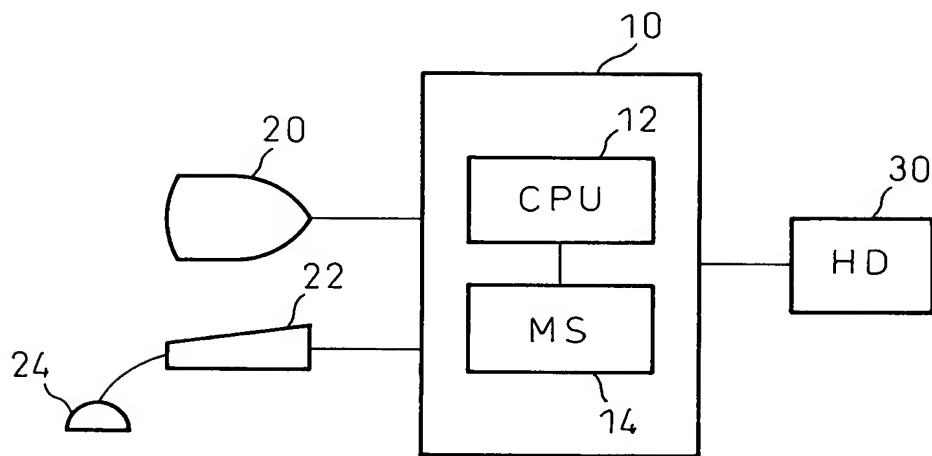


Fig.4

SOFTWARE COMPONENTS	DESIGN LOGIC DESCRIPTION LANGUAGE	METHOD OF VERIFICATION MODEL CREATION			VERIFICATION MODEL DESCRIPTION LANGUAGE
		CONVERSION 1*1	MODIFICATION*2	CONVERSION 2*3	
OS/MIDDLEWARE	ANSI-C	—	—	○	C-BASED LANGUAGE
INTERRUPT HANDLER	ANSI-C AND ASSEMBLY LANGUAGE	○	—	○	C-BASED LANGUAGE
DEVICE DRIVER	ANSI-C AND ASSEMBLY LANGUAGE	○	○	○	C-BASED LANGUAGE
TASK	ANSI-C	—	—	○	C-BASED LANGUAGE

*1: CONVERSION FROM ASSEMBLY LANGUAGE DESCRIPTION TO ANSI-C DESCRIPTION (MANUAL)

*2: MODIFICATION FOR "DIRECT READ/WRITE ACCESS TO BUS" (MANUAL)

*3: CONVERSION FROM UNTIMED SOFTWARE COMPONENT TO TIMED SOFTWARE COMPONENT (AUTOMATIC)

Fig. 5

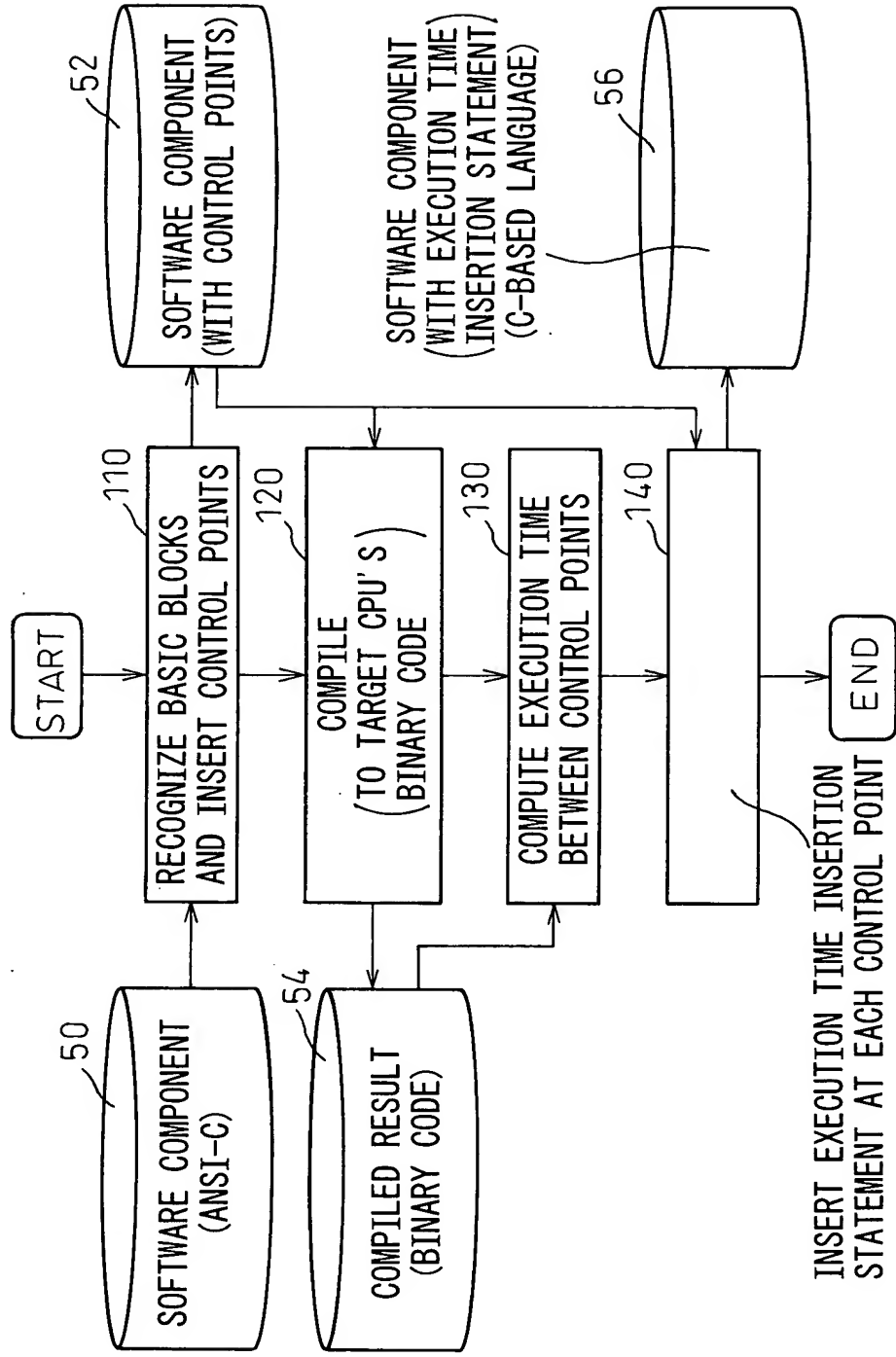


Fig. 6

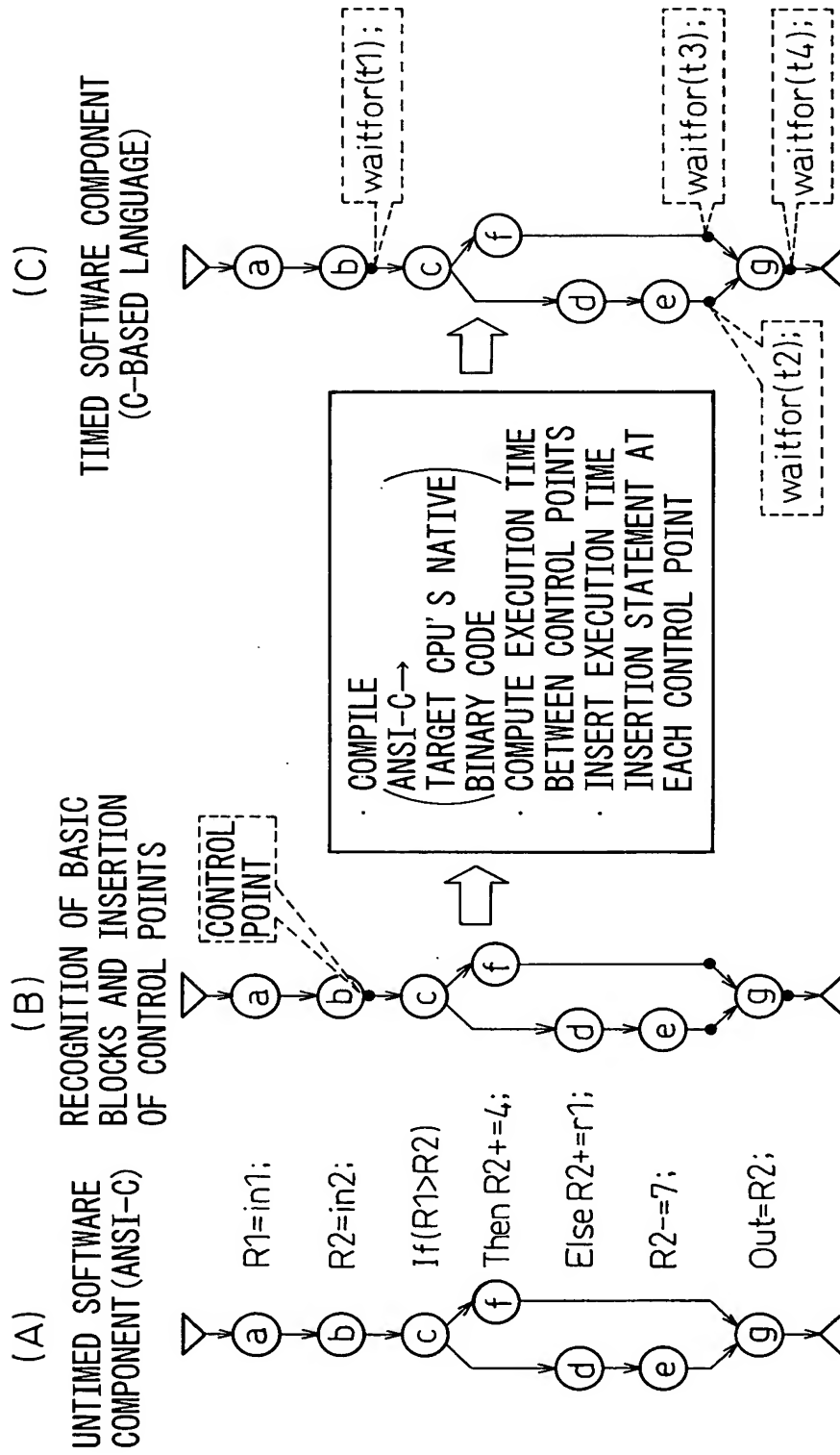


Fig. 7

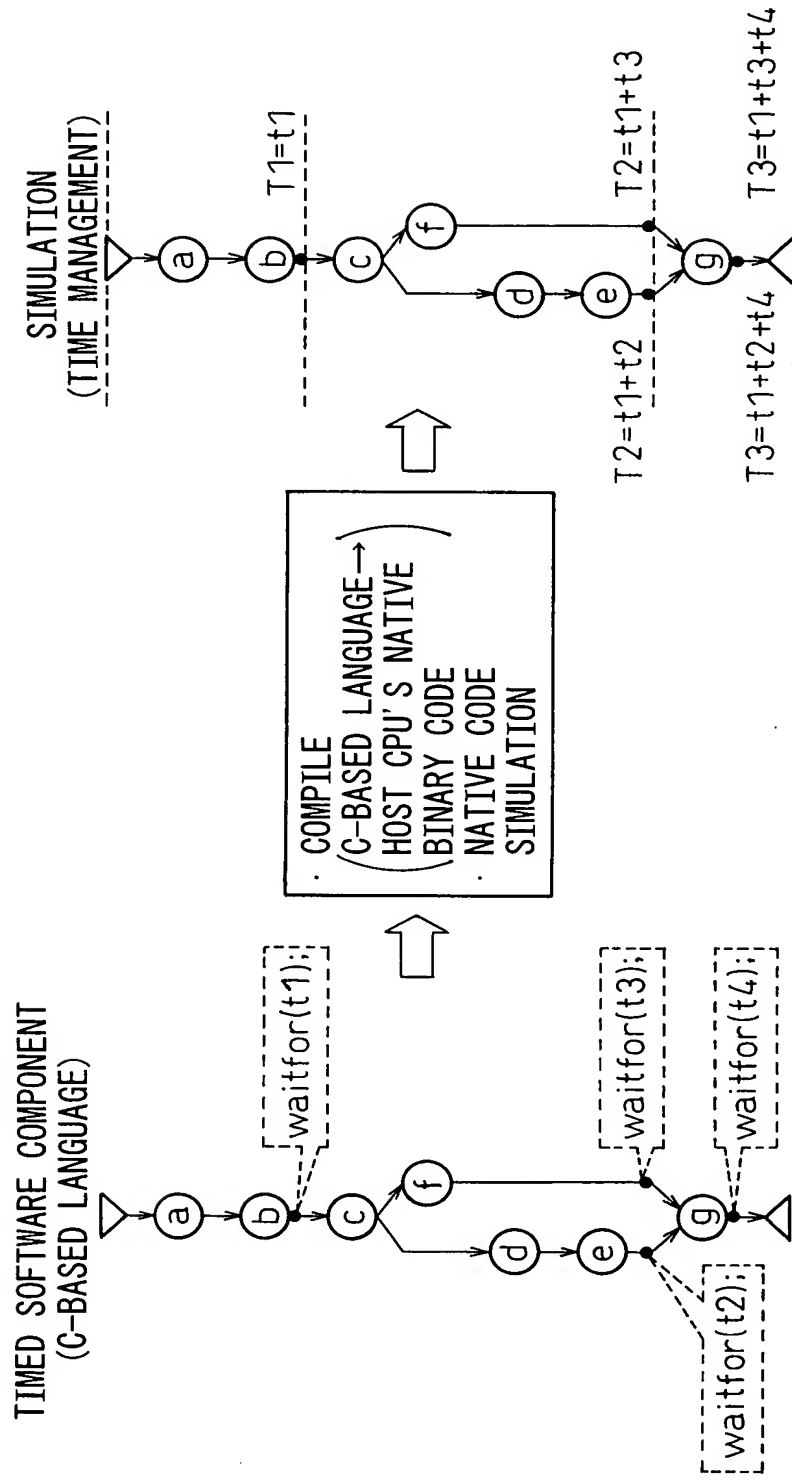


Fig. 8

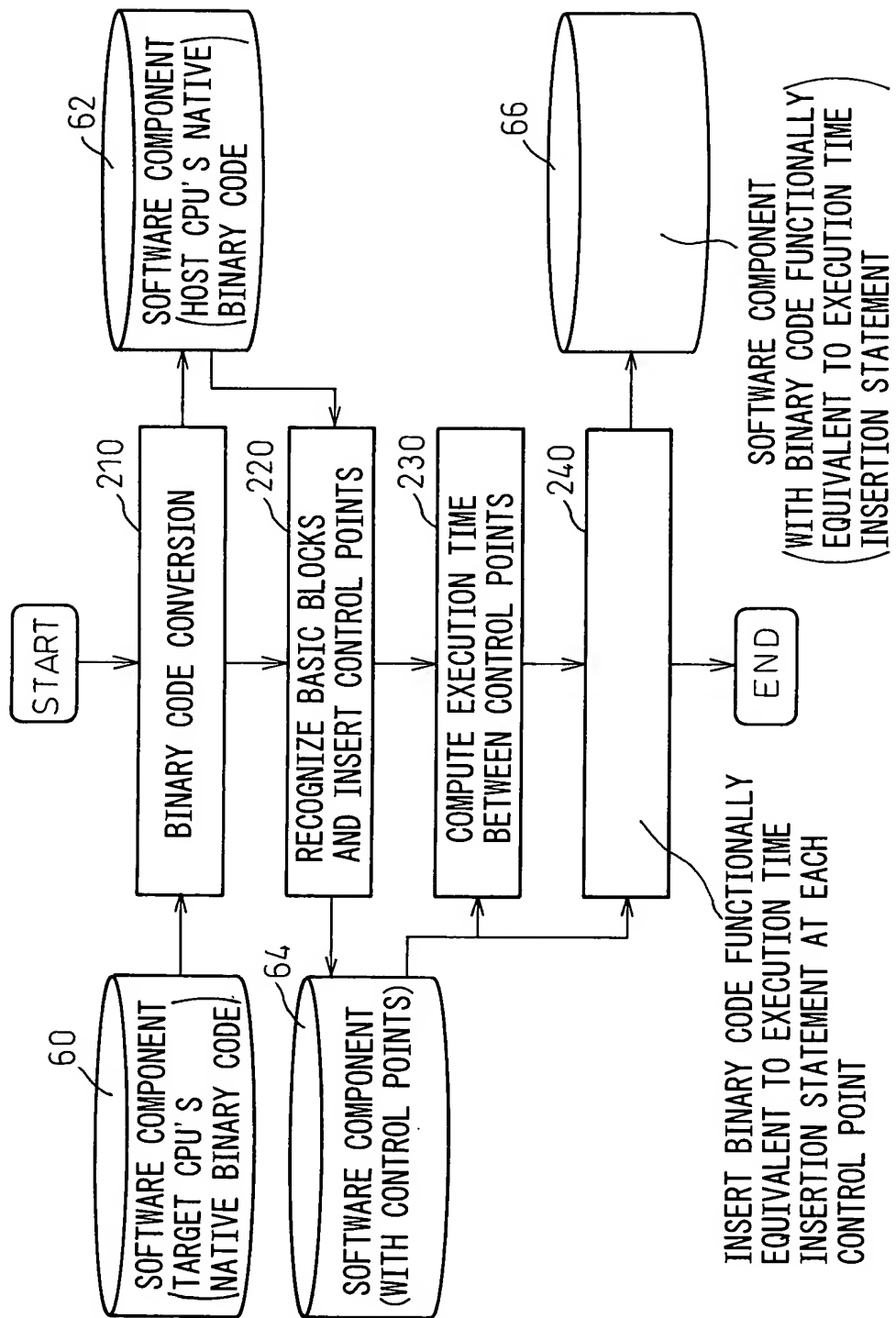


Fig.9

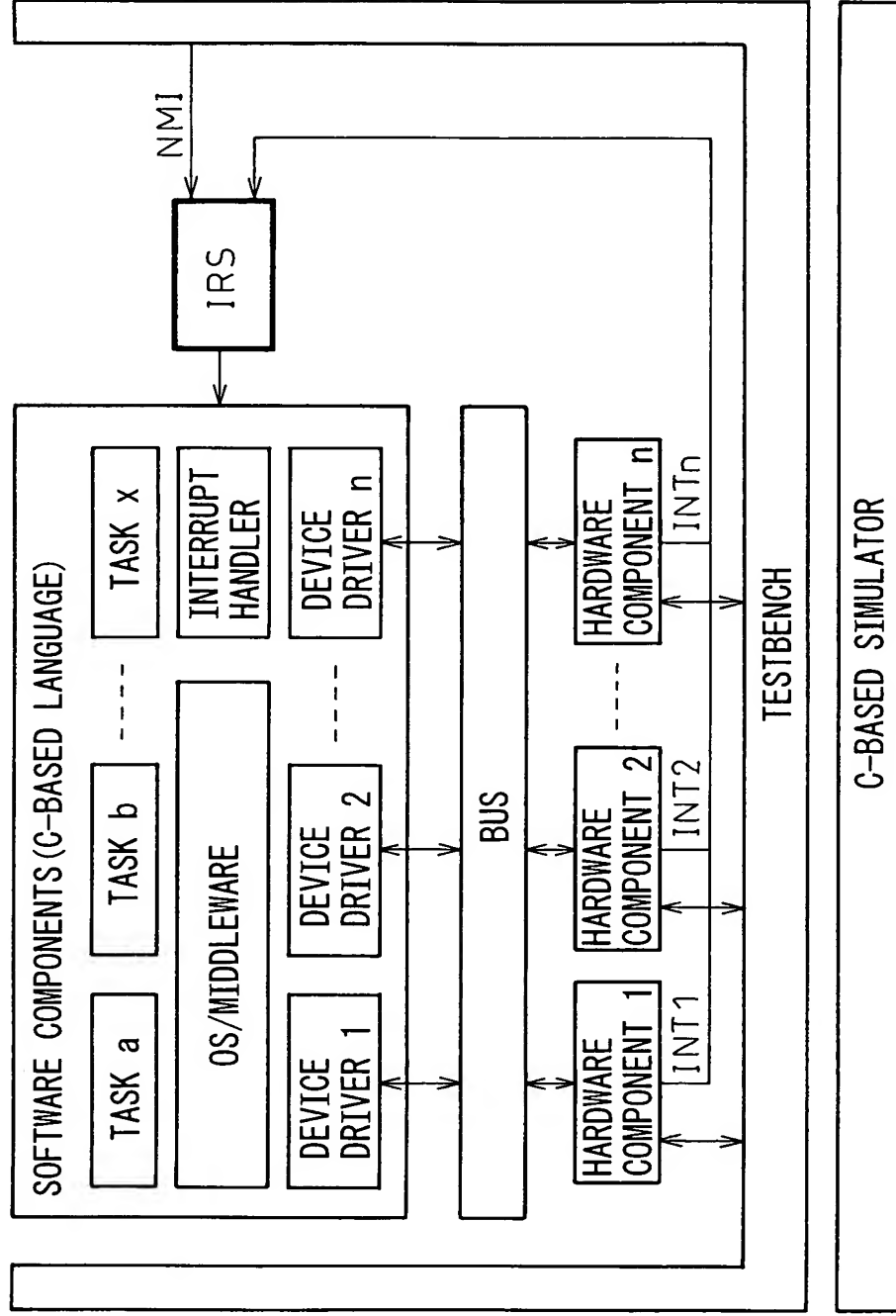


Fig.10

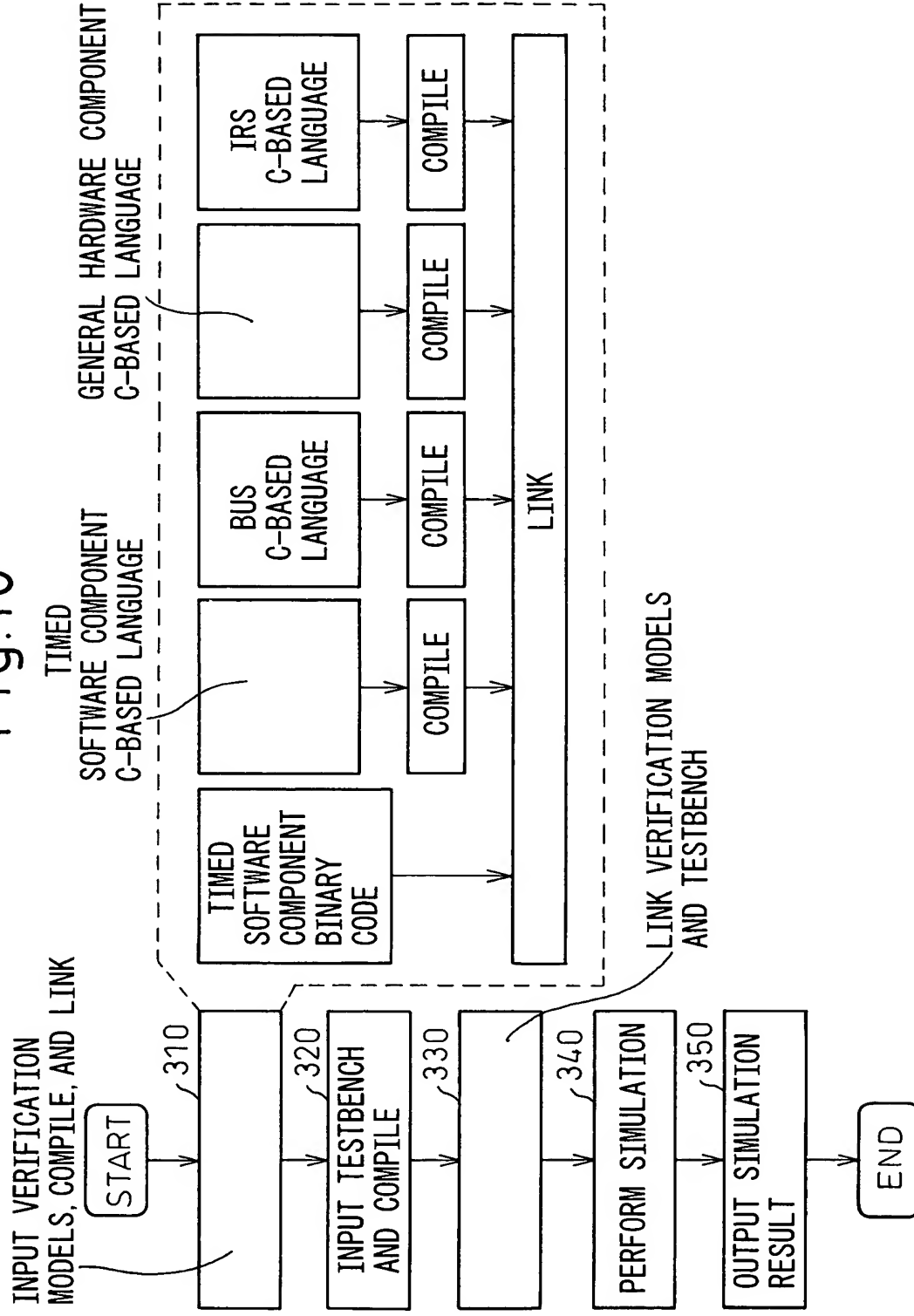


Fig.11

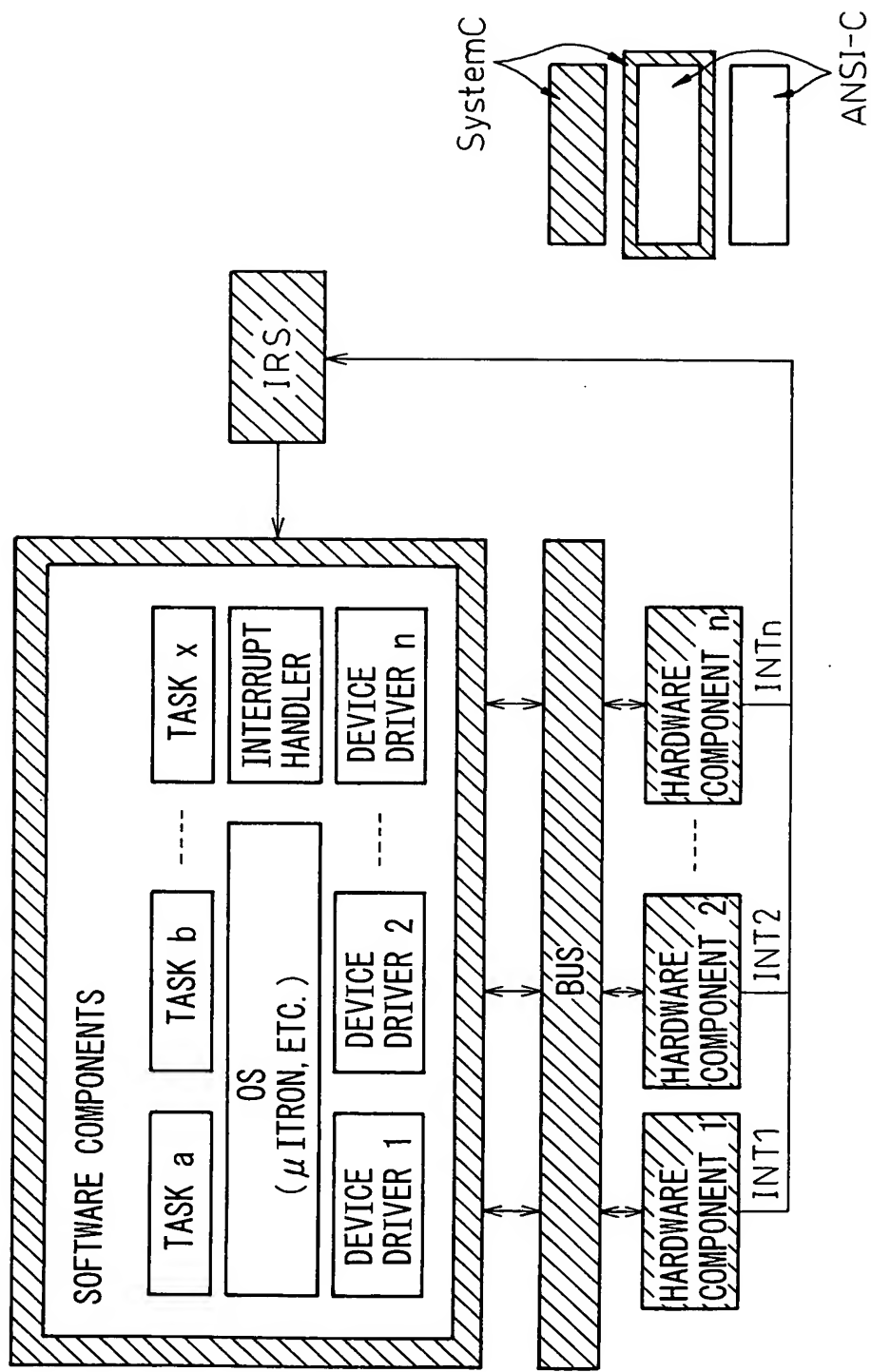


Fig.12

